

Parisa Rajesh Vispute

Pre-final year | Department of Electrical Engineering | Indian Citizen | [Linkedin](#)



PERSONAL INFORMATION

Email id: parisavispute11@gmail.com | Contact no.: +91 9209703787 | Date of Birth: 11/07/2005

Shri Ramdeobaba College Of Engineering And Management

Maharashtra, India

B.Tech. in Electrical Engineering, Micro Spl. in Electrical Vehicles

8.39/10

Related Coursework: Electrical Machines, Microcontroller(ATmega 32A), Electrical Panels, Designing, Machine Learning Algorithms, Matlab Simulation,PLC SCADA,(Siemens S7-1200) Electrical Vehicles- Value Added Course(In association with Morris Garages)

EXPERIENCE

Goenka Switchgears

Nagpur, India

Summer Intern

June'25-July'25

- Designed electrical control panel layouts in **AutoCAD**, optimizing devices' arrangement, resulting in fabrication-ready designs.
- Observed panel manufacturing during industry visits, including **switchgear setup**, enhancing practical knowledge of panel workflows.
- Researched PLC systems, **Master Trip relays**, and protection schemes, strengthening understanding of industrial automation and electrical safety practices.

Wooferrz Innovations Pvt Ltd.

Nagpur, India

Content Writing And Media Management Intern:

Nov'25-Feb'25

- Identified the need to **improve engagement** and reach for ongoing digital brand campaigns through better audience understanding.
- Analysed audience trends, managed and **segmented databases**, and collaborated with teams to develop targeted, high-quality content.
- Helped drive approximately **10%** growth in brand reach by improving **content relevance**, giving it the recognition it was aiming for.

PROJECTS

Fault Detection in Photovoltaic Systems using Machine Learning | Major Project

Jan'26-Present

Principal Guide : Dr. Uday Mujumdar (HOD)

- Developing a machine learning framework for **Photovoltaic fault detection** using the provided dataset, by implementing **Random Forest** classifier.
- Performing data preprocessing to improve model **robustness** and **detection accuracy**, training and evaluating the model for **predictive analysis**.

PWM Based Boost Converter Control | COURSE PROJECT

July'25 - Nov'25

Principal Guide : Dr. Uday Mujumdar (HOD)

- Designed a Pulse Width Modulation-controlled **DC-DC** boost converter using **ATmega32A** for regulated voltage step-up applications.
- Implemented Fast PWM (**62.5 kHz**) with ADC-based voltage feedback and proportional control using the microcontroller Embedded C.
- Developed and tested hardware using **IRFS40 MOSFET**, **Schottky diode**, inductor, and output filter to minimise ripple and losses.
- Validated stable output regulation, **duty-cycle control**, and **efficiency** under varying operating conditions through theoretical analysis.

Fire Alarm Using LM741 | COURSE PROJECT

Dec'23-May'24

Principal Guide : Sheetal Umredkar

- Tasked with designing a basic **analog fire detection system** using discrete components as part of an analog electronics lab mini-project.
- Detected abnormal temperature rise using a sensor and triggered a visual alert through **comparator-based decision** logic.
- Designed a thermistor-based sensing circuit using **LM741** op-amp as a comparator, with usage of a potentiometer and **LED indication**.
- Validated **temperature-triggered alarm activation**, validating comparator operation and sensor-based analog signal conditioning.

COMPETITIONS

Runner Up|ANADIGIX| KSHITIJ'2025 ,IIT KHARAGPUR

Nov'24 - Dec'24

Contactless Voltage Detector Using BC547 Transistors

- Designed a contactless AC voltage detector using **3-stage BC547**, ensuring amplification of AC electrical signals from live AC wires.
- Enabled detection of **230V AC** at **1-2 cm** distance using **electric-field sensing** via copper antenna, offering stable signal processing.
- Enabled low-power portable operation on a **3-5 V DC** supply, thus with compact design, it is aimed to be suitable for handheld applications
- Demonstrated stable **LED indication** with no false positives during functional testing with practical usage across industries and residences.

CERTIFICATIONS AND ACHIEVEMENTS

- 2025:** Google Cloud| IIM A|IBM| Arizona State University| Whizlabs | University Of Toronto |Imperial College London| IIT Kharagpur
- 2025:** 2nd Position in ANADIGIX, Kshitij '2025,IIT Kharagpur.

POSITIONS OF RESPONSIBILITY

Head Editor/ ARCEON-College Annual Magazine

Oct'25 - Present.

- Directing the annual college magazine by orchestrating entries across **10** domains and closing the participation at more than **150** entries.
- Curating theme-specific content, thus monitoring media that impacted **audience engagement** across social media platforms(increased by **~17%**)

Joint Secretary/EESA: Electrical Engineering Students' Association

Jul'25-Present

Member/ Readers' Reverie Literary Club

Nov'23-Present

SKILLS AND EXPERTISE

Languages: Python | C | C++ | HTML | CSS | Javascript | **Libraries/Frameworks/Tools:** | MS-Office | Canva | Google Colab | Jupyter Notebook | Matlab Simulation| AutoCad| Pandas | NumPy |Solidworks

Skills: Excel | DBMS | OOPs | OS | PLC SCADA